

REPLACEMENT SHEET



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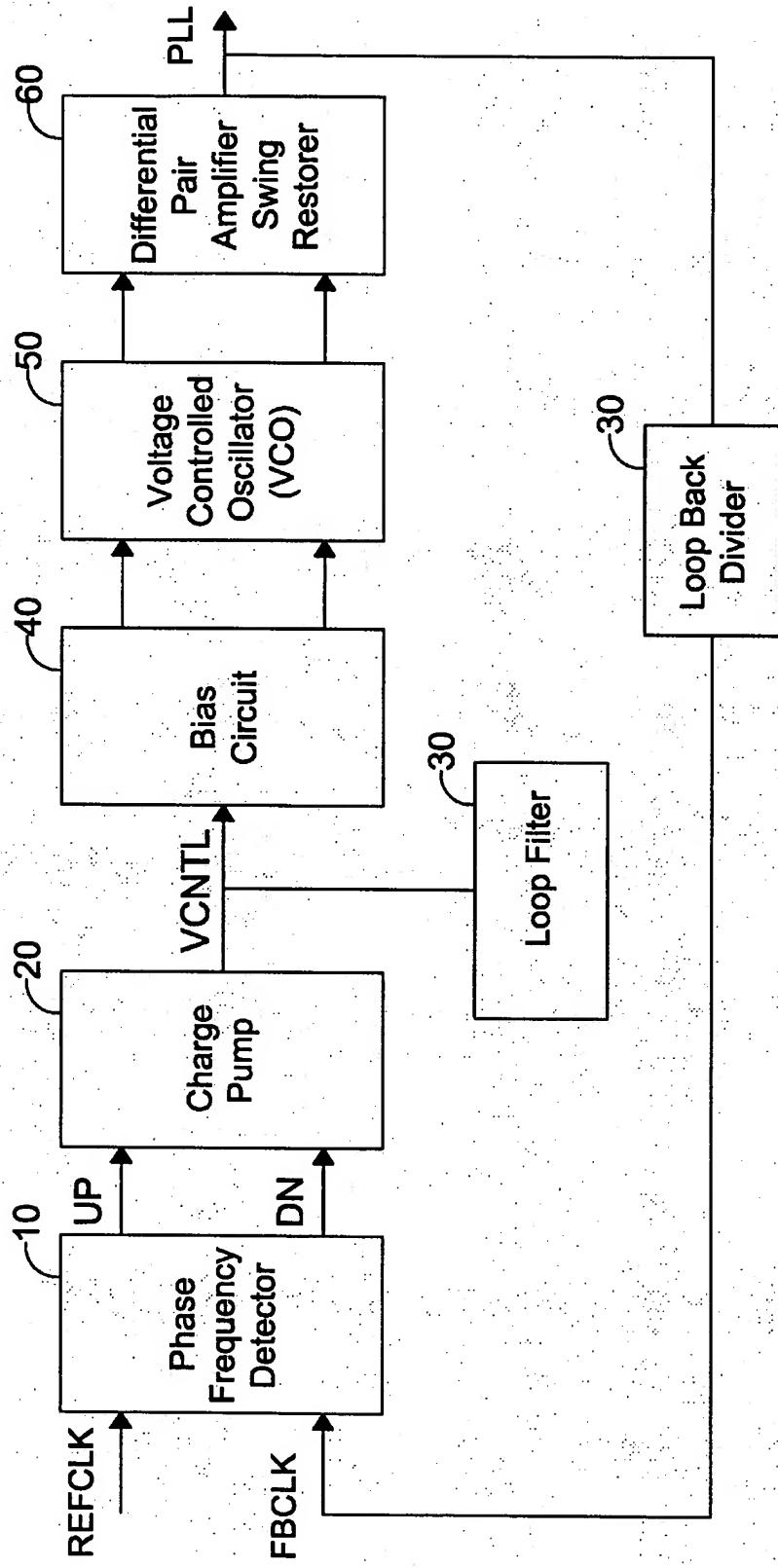


FIG. 1

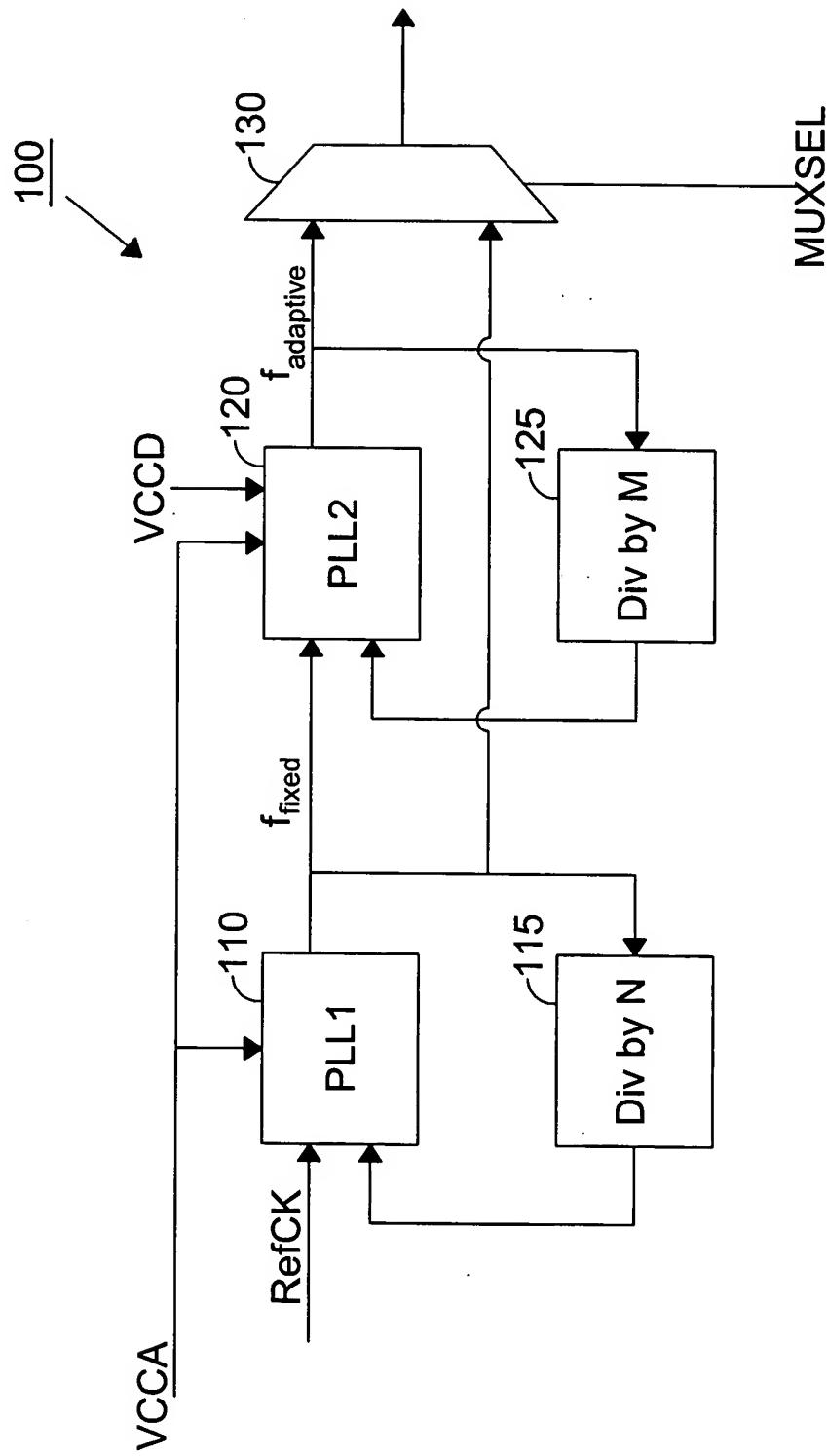


FIG. 2

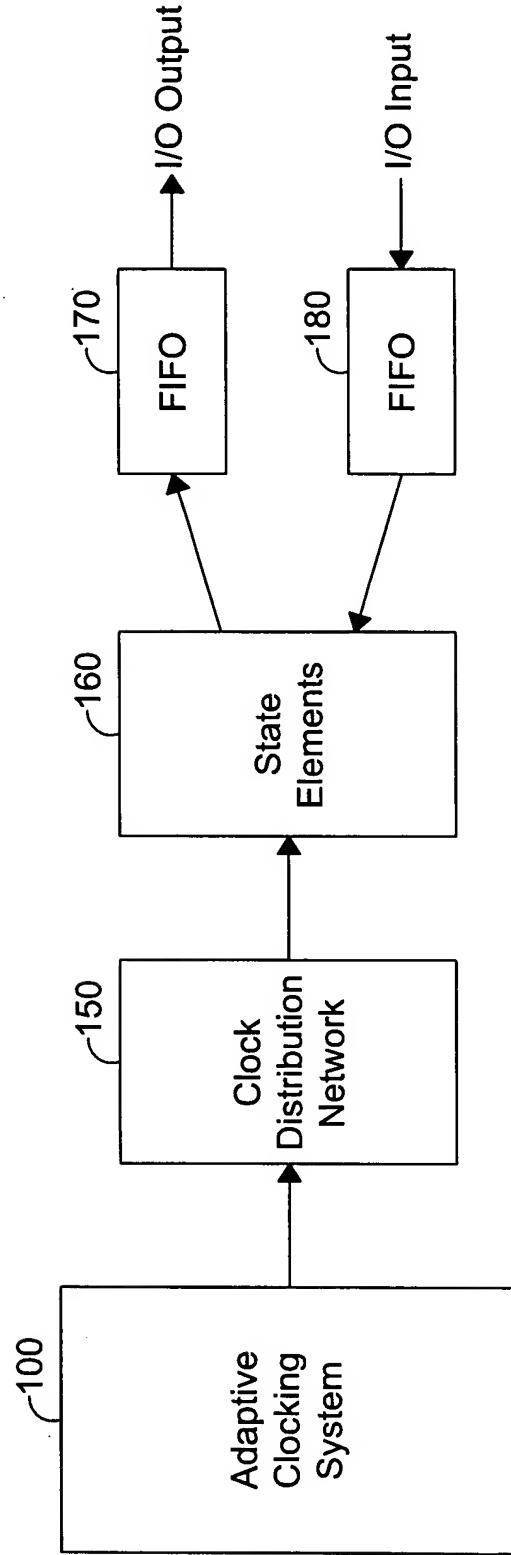


FIG. 3

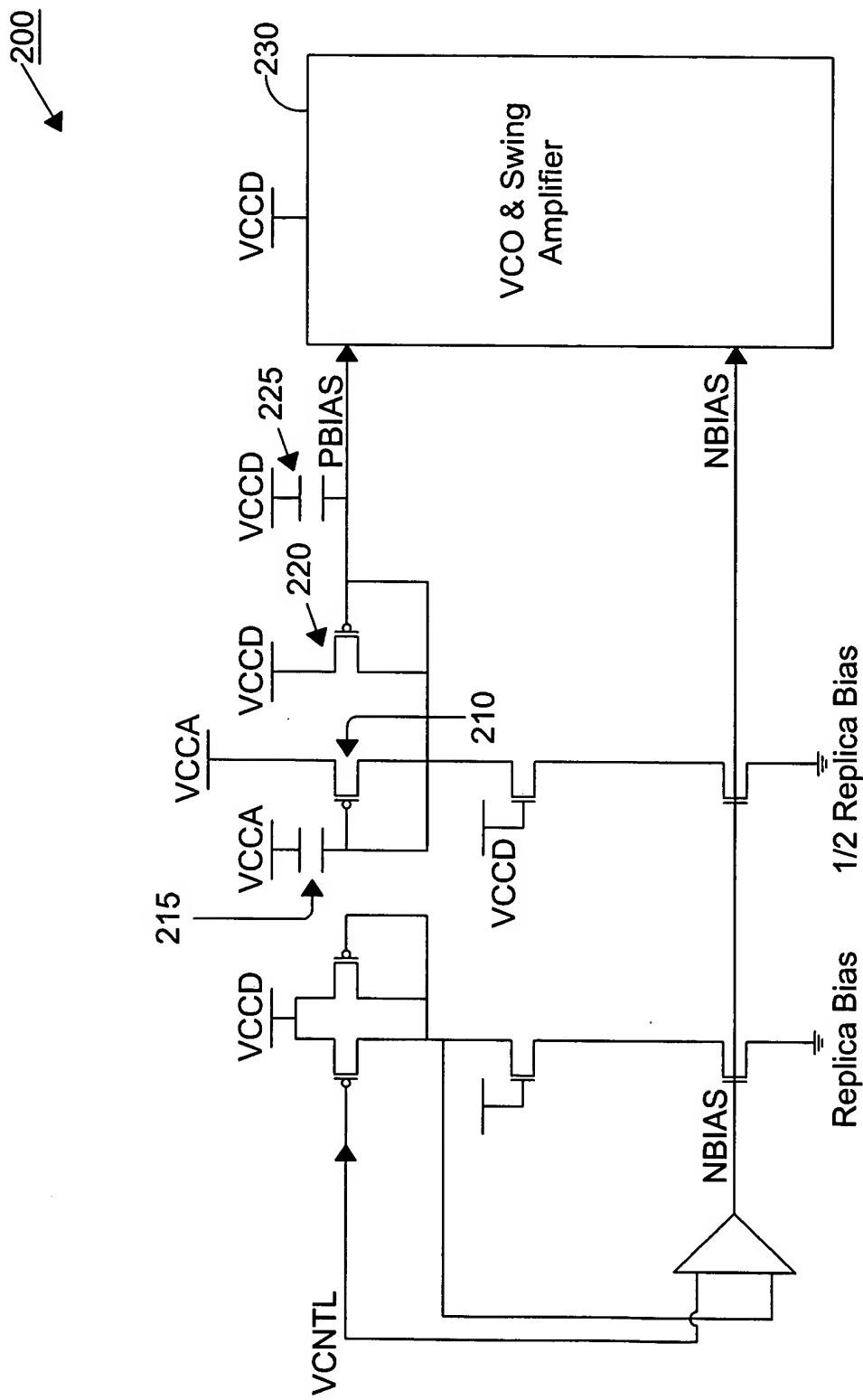


FIG. 4

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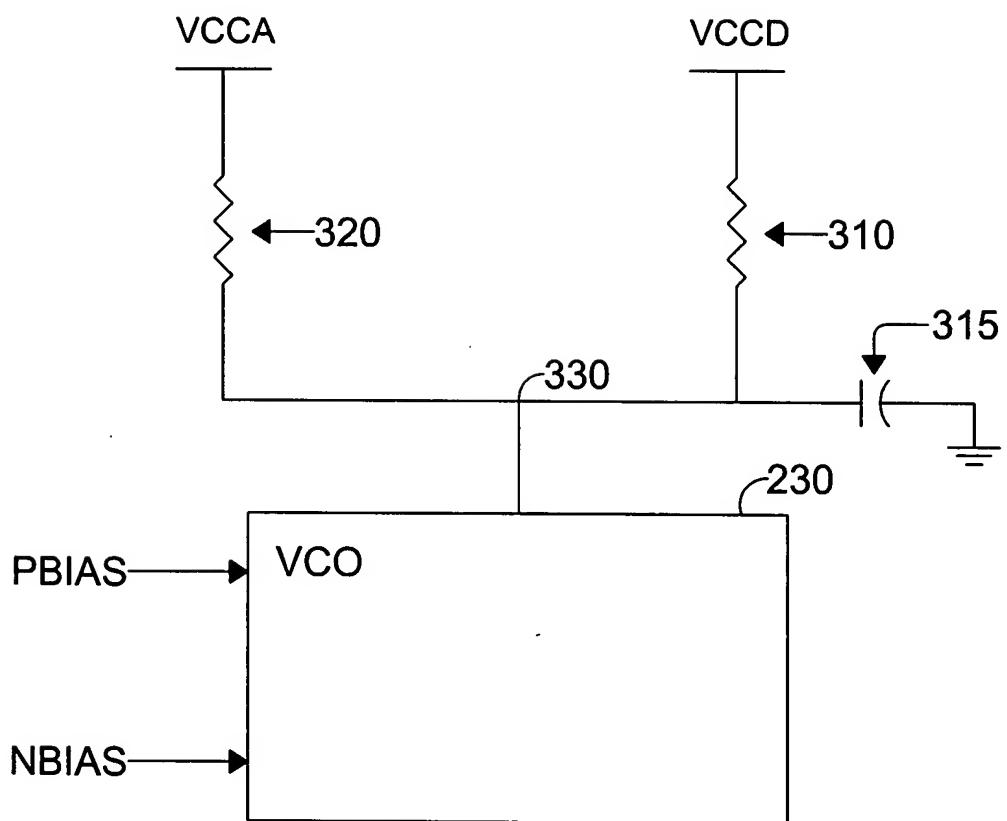


FIG. 5

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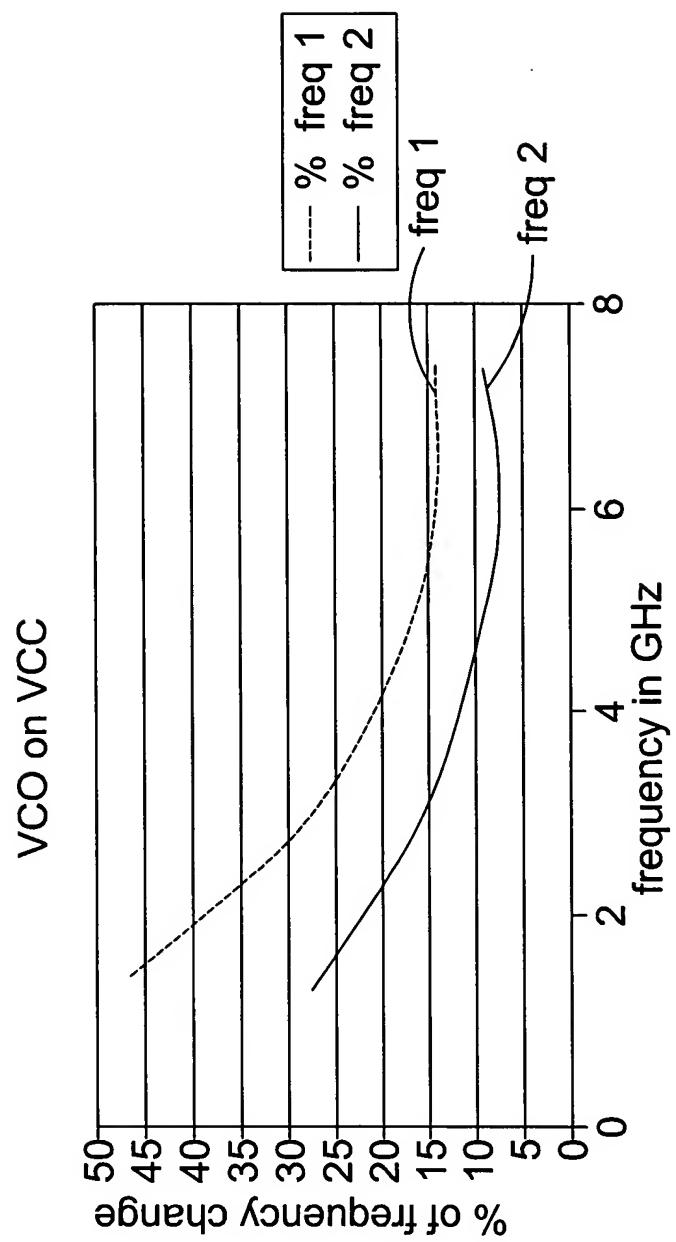


FIG. 6

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Analog Adaptive PLL

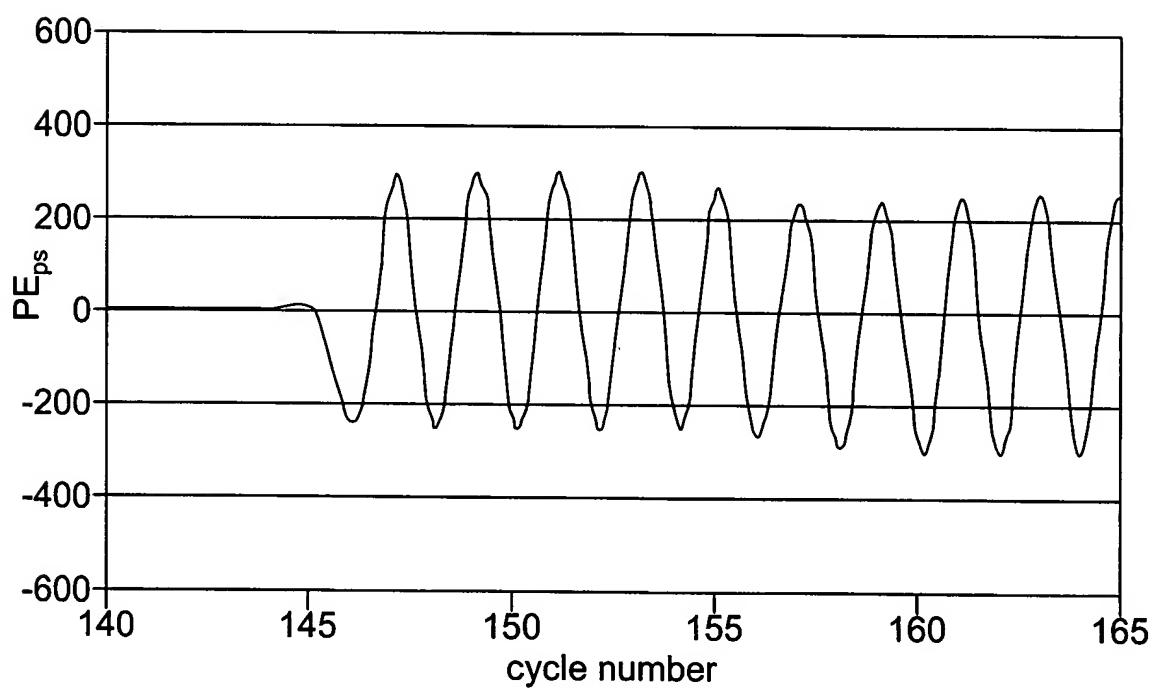
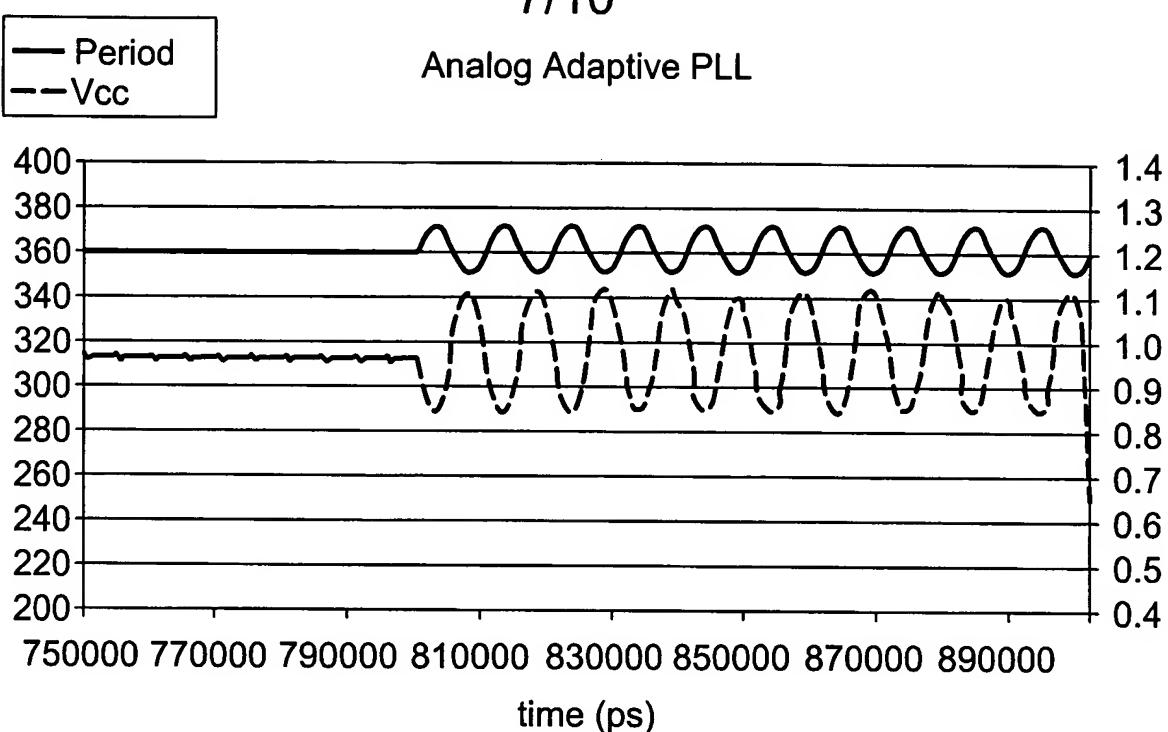


FIG. 7

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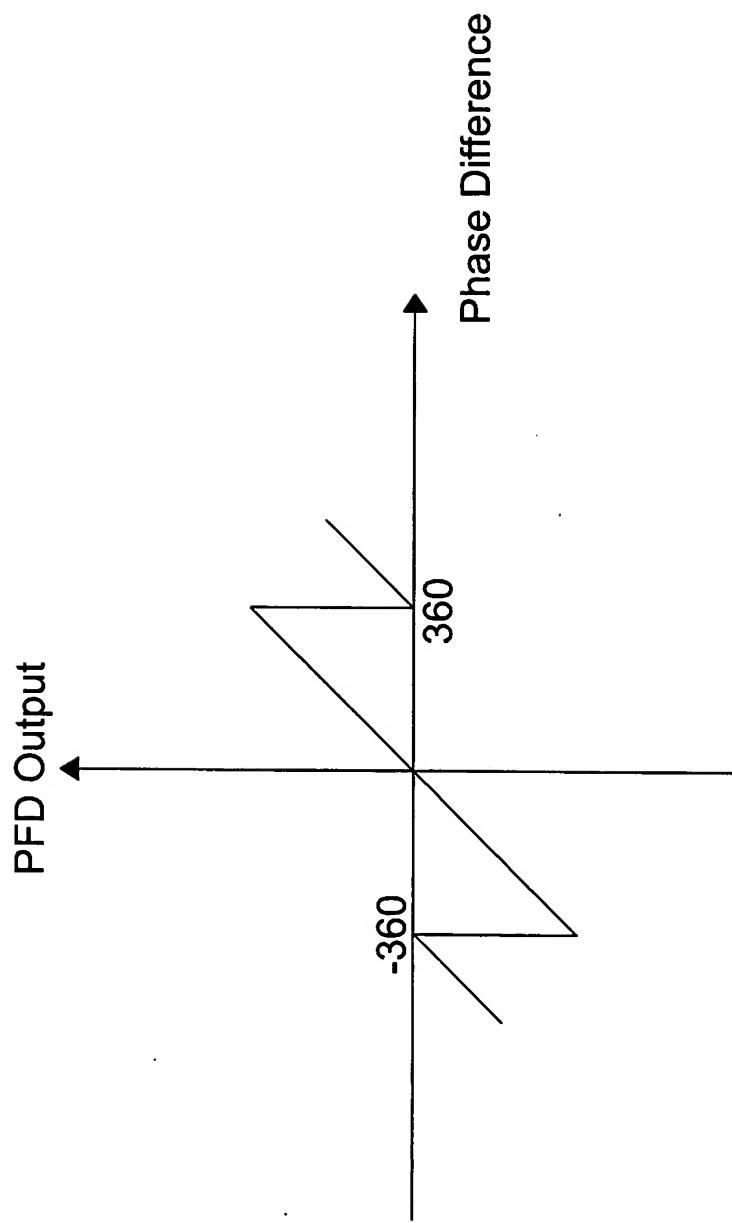


FIG. 8

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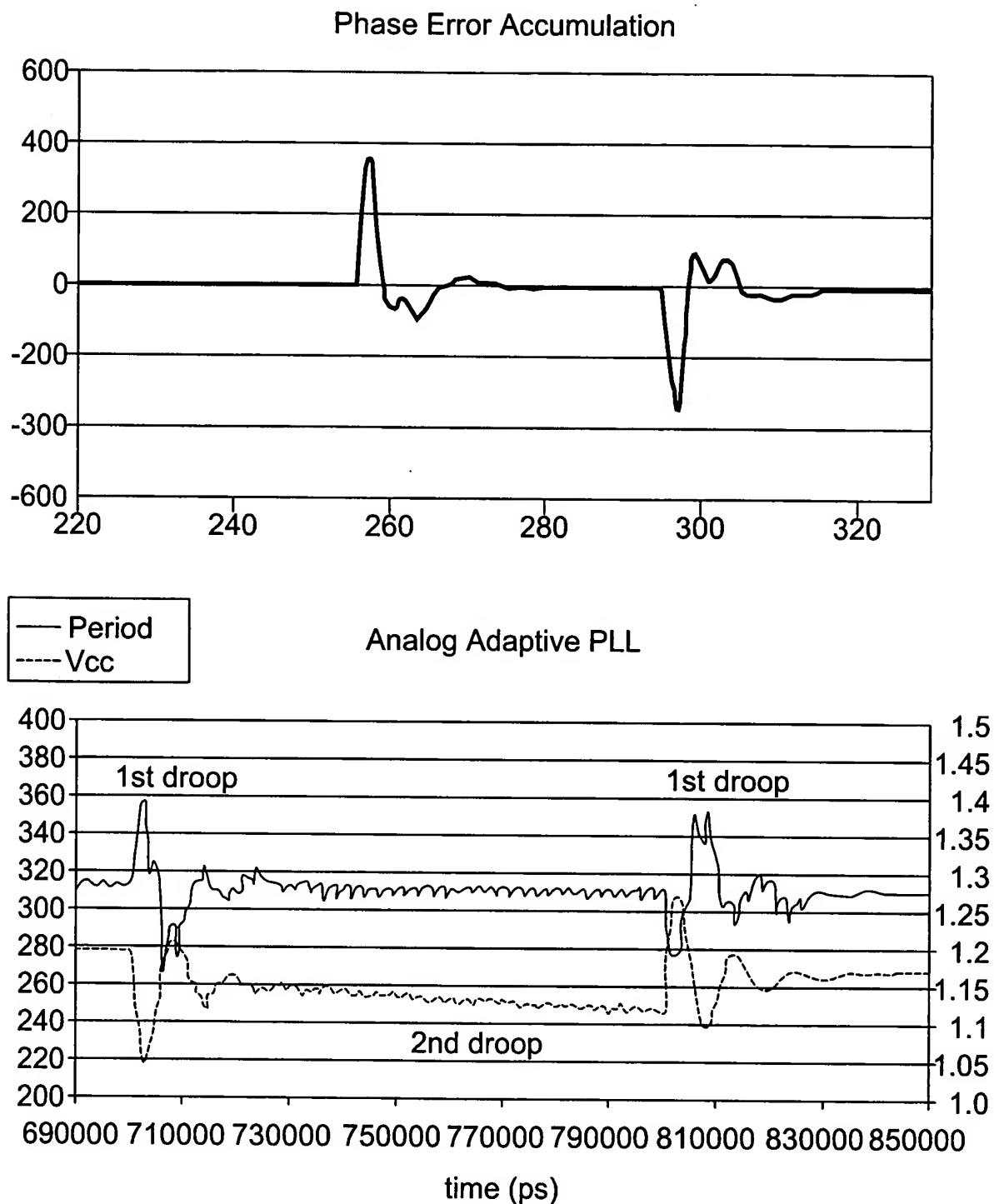


FIG. 9

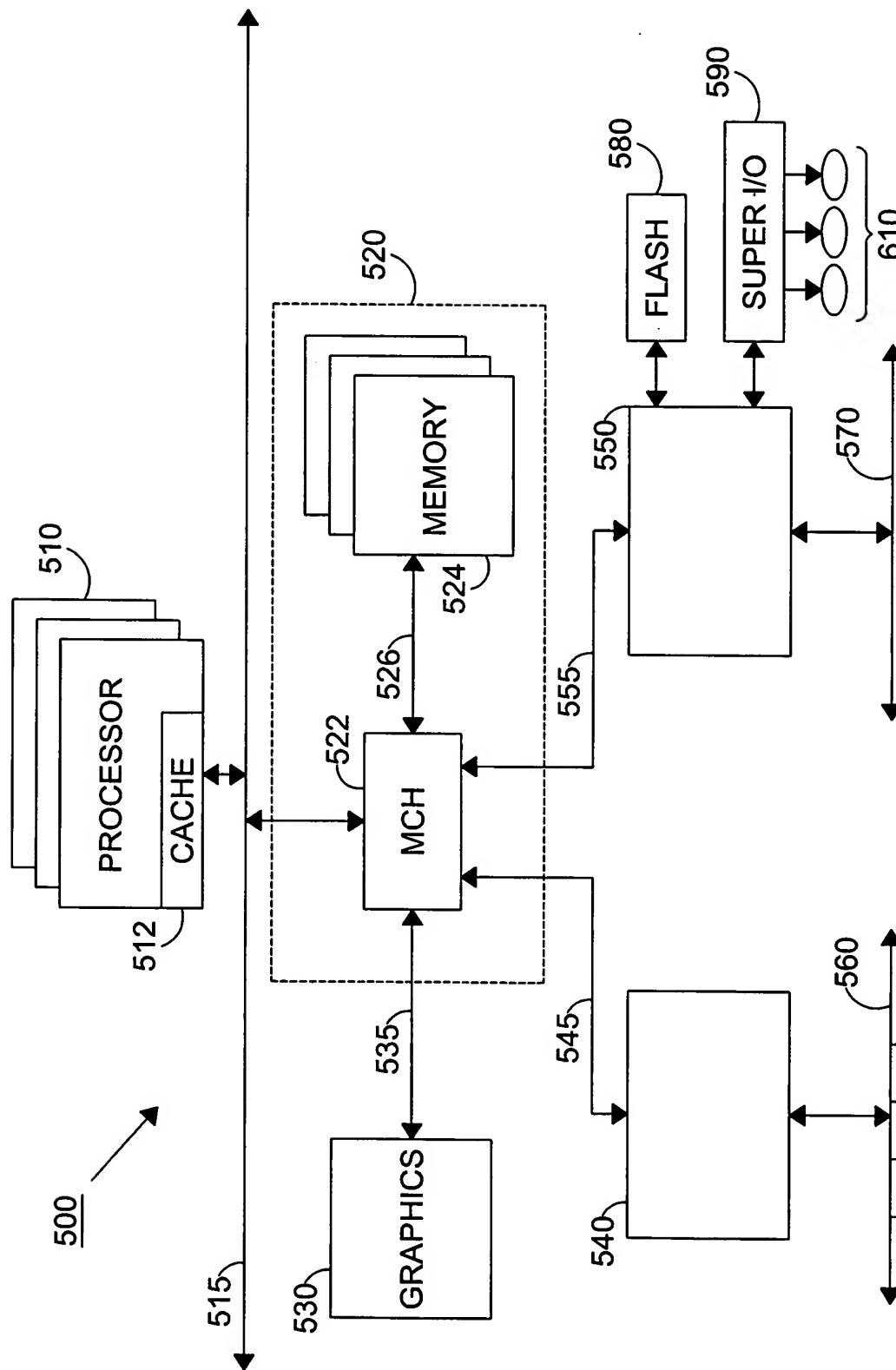


FIG. 10